General Purpose Transistors

NPN Silicon

Features

• These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
|--------------------------------|------------------|-------|------|
| Collector-Emitter Voltage | V _{CEO} | 32 | Vdc |
| Collector-Base Voltage | V _{CBO} | 32 | Vdc |
| Emitter-Base Voltage | V _{EBO} | 5.0 | Vdc |
| Collector Current – Continuous | Ι _C | 100 | mAdc |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

THERMAL CHARACTERISTICS

| Characteristic | Symbol | Value | Unit |
|---|-----------------------------------|-------------|-------|
| Total Device Dissipation FR-5 Board ⁽¹⁾ $T_{\Delta} = 25^{\circ}C$ | P _D | 225 | mW |
| Derate above 25°C | | 1.8 | mW/°C |
| Thermal Resistance, Junction-to-Ambient | $R_{\theta JA}$ | 556 | °C/W |
| Total Device Dissipation Alumina Substrate, ⁽²⁾ $T_A = 25^{\circ}C$ | PD | 300 | mW |
| Derate above 25°C | | 2.4 | mW/°C |
| Thermal Resistance, Junction-to-Ambient | $R_{\theta JA}$ | 417 | °C/W |
| Junction and Storage Temperature | T _J , T _{stg} | –55 to +150 | °C |

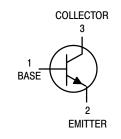
1. FR-5 = $1.0 \times 0.75 \times 0.062$ in.

2. Alumina = 0.4 \times 0.3 \times 0.024 in. 99.5% alumina.



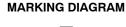
ON Semiconductor®

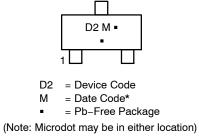
http://onsemi.com





SOT-23 (TO-236AB) CASE 318 STYLE 6





*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

| Device | Package | Shipping [†] |
|-----------|---------------------|-----------------------|
| BCW32LT1G | SOT-23 (Pb-Free) | 3000 / Tape & Reel |

+ For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

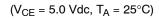
| Characteristic | Symbol | Min | Тур | Max | Unit |
|--|----------------------|-----|-----|-----------|--------------|
| OFF CHARACTERISTICS | | | | | |
| Collector – Emitter Breakdown Voltage $(I_C = 2.0 \text{ mAdc}, V_{EB} = 0)$ | V _{(BR)CEO} | 32 | - | _ | Vdc |
| Collector – Base Breakdown Voltage $(I_C = 10 \ \mu Adc, I_E = 0)$ | V _{(BR)CBO} | 32 | - | - | Vdc |
| Emitter – Base Breakdown Voltage ($I_E = 10 \ \mu Adc, I_C = 0$) | V _{(BR)EBO} | 5.0 | - | - | Vdc |
| Collector Cutoff Current (V_{CB} = 32 Vdc, I _E = 0) (V_{CB} = 32 Vdc, I _E = 0, T _A = 100°C) | I _{CBO} | | | 100 10 | nAdc μAdc |
| ON CHARACTERISTICS | | | • | • | |
| DC Current Gain (I _C = 2.0 mAdc, V _{CE} = 5.0 Vdc) | h _{FE} | 200 | _ | 450 | - |
| Collector Emitter Seturation Valtage | N/ | | | | Vala |

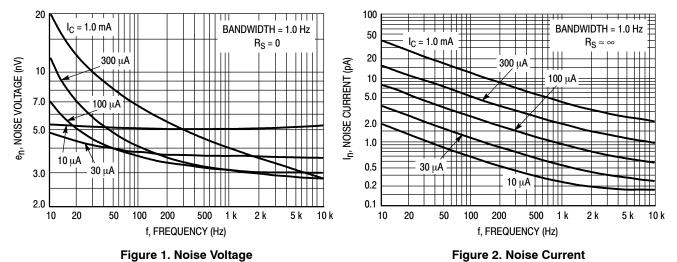
| (1C = 2.0 MAde, VCE = 3.0 Vde) | | 200 | _ | 430 | | |
|--|----------------------|------|---|------|-----|---|
| Collector – Emitter Saturation Voltage | V _{CE(sat)} | | | | Vdc | |
| $(I_C = 10 \text{ mAdc}, I_B = 0.5 \text{ mAdc})$ | | - | - | 0.25 | | l |
| Base – Emitter On Voltage | V _{BE(on)} | | | | Vdc | I |
| (I _C = 2.0 mAdc, V _{CE} = 5.0 Vdc) | | 0.55 | - | 0.70 | | 1 |

SMALL-SIGNAL CHARACTERISTICS

| Output Capacitance (I _E = 0, V _{CB} = 10 Vdc, f = 1.0 MHz) | C _{obo} | - | - | 4.0 | pF |
|---|------------------|---|---|-----|----|
| Noise Figure (I _C = 0.2 mAdc, V _{CE} = 5.0 Vdc, R _S = 2.0 k Ω , f = 1.0 kHz, BW = 200 Hz) | NF | - | - | 10 | dB |

TYPICAL NOISE CHARACTERISTICS





NOISE FIGURE CONTOURS

(V_CE = 5.0 Vdc, T_A = 25°C)

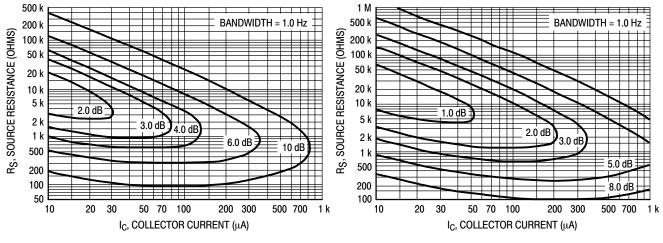
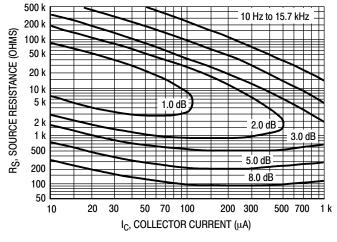
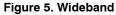


Figure 3. Narrow Band, 100 Hz





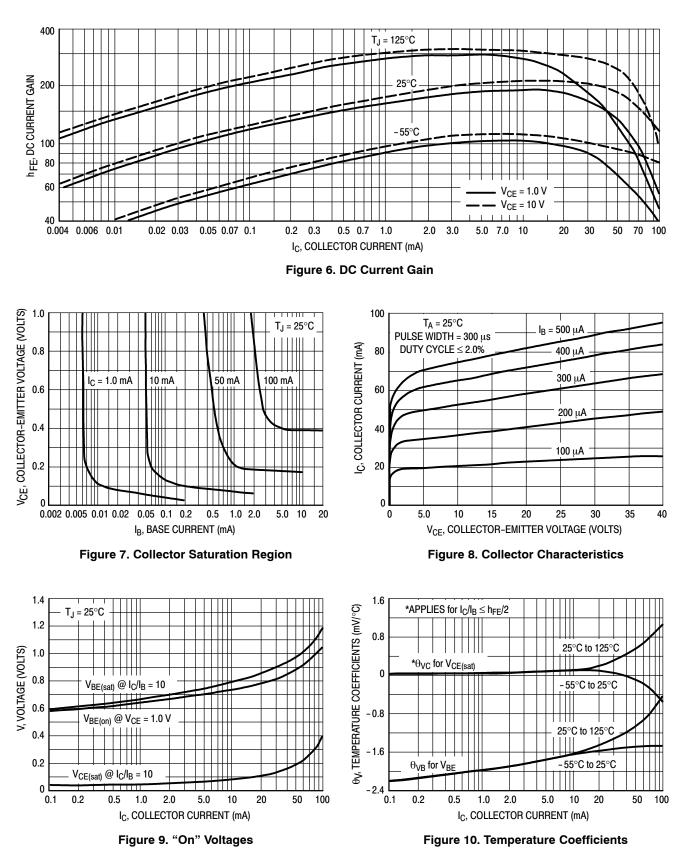


Noise Figure is defined as:

$$NF = 20 \log_{10} \left(\frac{e_{n}^{2} + 4KTR_{S} + I_{n}^{2}R_{S}^{2}}{4KTR_{S}} \right)^{1/2}$$

- e_n = Noise Voltage of the Transistor referred to the input. (Figure 3) I = Noise Current of the Transistor referred to the input. _n (Figure 4)
 - $K = Boltzman's Constant (1.38 x 10^{-23} j/^{\circ}K)$
 - T = Temperature of the Source Resistance ($^{\circ}$ K)
 - $R = Source Resistance (\Omega)$
 - s

TYPICAL STATIC CHARACTERISTICS



TYPICAL DYNAMIC CHARACTERISTICS

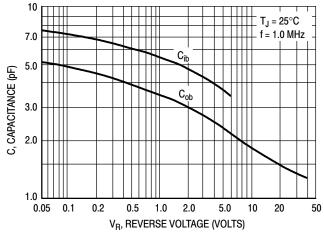
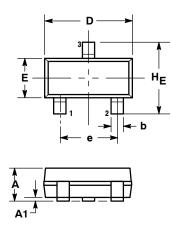
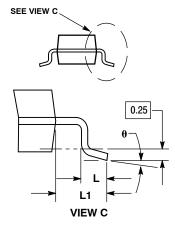


Figure 11. Capacitance

PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 **ISSUE AN**





NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

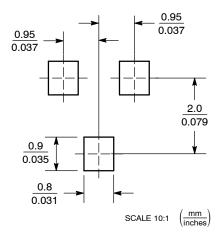
- 2.
- 3.
- ANSI Y 14.300, 1992. 2. CONTROLLING DIMENSION: INCH. 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF

| | м | ILLIMETE | RS | | | |
|-----|------|----------|------|-------------|---------|-------|
| DIM | MIN | NOM | MAX | MIN | MIN NOM | |
| Α | 0.89 | 1.00 | 1.11 | 0.035 | 0.040 | 0.044 |
| A1 | 0.01 | 0.06 | 0.10 | 0.001 | 0.002 | 0.004 |
| b | 0.37 | 0.44 | 0.50 | 0.015 | 0.018 | 0.020 |
| с | 0.09 | 0.13 | 0.18 | 0.003 | 0.005 | 0.007 |
| D | 2.80 | 2.90 | 3.04 | 0.110 | 0.114 | 0.120 |
| E | 1.20 | 1.30 | 1.40 | 0.047 | 0.051 | 0.055 |
| е | 1.78 | 1.90 | 2.04 | 0.070 | 0.075 | 0.081 |
| L | 0.10 | 0.20 | 0.30 | 0.004 | 0.008 | 0.012 |
| L1 | 0.35 | 0.54 | 0.69 | 0.014 | 0.021 | 0.029 |
| HE | 2.10 | 2.40 | 2.64 | 0.083 0.094 | | 0.104 |

STYLE 6: PIN 1. BASE

2. EMITTER COLLECTOR 3

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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